

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Makoto NAKANISHI

Serial No. 10/798,287

Group Art Unit: 2193

Confirmation No. 6442

Filed: March 12, 2004

Examiner: Chuong D. Ngo

For: MATRIX PROCESSING DEVICE IN SMP NODE DISTRIBUTED MEMORY TYPE
PARALLEL COMPUTER

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed December 29, 2008, and having a period for response set to expire on March 29, 2009.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.